10246912	7096470	20040060052	150	09/19/2002	METHOD AND APPARATUS FOR IMPLEMENTING THREAD REPLACEMENT FOR OPTIMAL PERFORMANCE IN A TWO-TIERED MULTITHREADING STRUCTURE	TO,JENNIFER	BROWN, JEFFREY DOUGLAS
10251276	7194586	20040059877	150	09/20/2002	METHOD AND APPARATUS FOR IMPLEMENTING CACHE STATE AS HISTORY OF READ/WRITE SHARED DATA	KIM,DANIEL	BROWN, JEFFREY DOUGLAS
10313682	7124257	20040111550	150	12/05/2002	BUS INTERFACE CONTROLLER FOR DETERMINING ACCESS COUNTS	MCLEAN MAYO,KIMBERLY	BROWN, JEFFREY DOUGLAS
10318594	7062612	20040117560	150	12/12/2002	UPDATING REMOTE LOCKED CACHE	ANDERSON,MATTHEW	BROWN, JEFFREY DOUGLAS
10346293	7096289	20040143691	150	01/16/2003	SENDER TO RECEIVER REQUEST RETRY METHOD AND APPARATUS	STIGLIC,RYAN	BROWN, JEFFREY DOUGLAS
10464882	7107363	20040260746	150	06/19/2003	MICROPROCESSOR HAVING BANDWIDTH MANAGEMENT FOR COMPUTING APPLICATIONS AND RELATED METHOD OF MANAGING BANDWIDTH ALLOCATION	KIM,HAROLD	BROWN, JEFFREY DOUGLAS
10631537	7356713	20050028015	150	07/31/2003	METHOD AND APPARATUS FOR MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	BROWN,MICHAEL	BROWN, JEFFREY DOUGLAS
10674977	Not Issued	20050068966	41	09/30/2003	Centralized bandwidth management method and apparatus	GREY,CHRISTOPHER	BROWN, JEFFREY DOUGLAS
10718936	Not Issued	20050111478	71	11/20/2003	Distributed control load shaping method and apparatus	PASIA,REDENTOR	BROWN, JEFFREY DOUGLAS
10730952	7099975	20050125581	150	12/09/2003	IMPROVED METHOD OF RESOURCE ARBITRATION	DANG,KHANH	BROWN, JEFFREY DOUGLAS
10912481	7299372	20060031836	150	08/05/2004	HIERARCHICAL MANAGEMENT FOR MULTIPROCESSOR SYSTEM WITH REAL-TIME ATTRIBUTES	DU,THUAN	BROWN, JEFFREY DOUGLAS
11077330	Not Issued	20060206657	124	03/10/2005	Single port/multiple ring implementation of a hybrid crossbar partially non-blocking data switch	VU,TRISHA	BROWN, JEFFREY DOUGLAS
11098621	Not Issued	20060251070	71	04/04/2005	Methods and apparatus for facilitating coherency management in distributed multi-processor system	GIARDINO JR,MARK	BROWN, JEFFREY DOUGLAS
11348969	Not Issued	20070186052	83	02/07/2006	Methods and apparatus for reducing command processing latency while maintaining coherence	THAMMAVONG,PRASITH	BROWN, JEFFREY DOUGLAS
11559090	Not Issued	20080114942	30	11/13/2006	Dynamic Data Cache Invalidate with Data Dependent Expiration	RUTZ,JARED	BROWN, JEFFREY DOUGLAS
11563772	Not Issued	20080122845	30	11/28/2006	Dynamic Determination of Optimal Spatial Index Mapping to Processor Thread Resources	CHOW,JEFFREY	BROWN, JEFFREY DOUGLAS
11563978	Not Issued	20080122841	30	11/28/2006	Ray Tracing Image Processing System	CHOW,JEFFREY	BROWN, JEFFREY DOUGLAS
11564010	Not Issued	20080122853	30	11/28/2006	Dynamic Spatial Index Remapping for Optimal Aggregate Performance	MA,TIZE	BROWN, JEFFREY DOUGLAS
11564030	Not Issued	20080122846	30	11/28/2006	Adaptive Ray Data Reorder for Optimized Ray Temporal Locality	MA,TIZE	BROWN, JEFFREY DOUGLAS

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11627464	Not Issued	20080180441	30	01/26/2007	Stochastic Culling of Rays with Increased Depth of Recursion	HAJNIK,DANIEL	BROWN, JEFFREY DOUGLAS
11668522	Not Issued	20080180442	30	01/30/2007	Stochastic Addition of Rays in a Ray Tracing Image Processing System	CHAUHAN,ULKA	BROWN, JEFFREY DOUGLAS
11693221	Not Issued	20080238920	30	03/29/2007	Color Buffer Contrast Threshold for Adaptive Anti-Aliasing	CHAUHAN,ULKA	BROWN, JEFFREY DOUGLAS
11776731	Not Issued	20090019228	30	07/12/2007	Data Cache Invalidate with Data Dependent Expiration Using a Step Value	BRAGDON,REGINALD	BROWN, JEFFREY DOUGLAS
11830928	Not Issued	20090033653	30	07/31/2007	Adaptive Sub-Sampling for Reduction in Issued Rays	CHAUHAN,ULKA	BROWN, JEFFREY DOUGLAS
11846697	Not Issued	20080052472	30	08/29/2007	METHODS AND APPARATUS FOR REDUCING COMMAND PROCESSING LATENCY WHILE MAINTAINING COHERENCE	BRAGDON,REGINALD	BROWN, JEFFREY DOUGLAS
11854617	Not Issued	20090073167	30	09/13/2007	Cooperative Utilization of Spacial Indices Between Application and Rendering Hardware	CHAUHAN,ULKA	BROWN, JEFFREY DOUGLAS
12025649	Not Issued	20080126817	41	02/04/2008	METHOD AND APPARATUS FOR MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	BROWN,MICHAEL	BROWN, JEFFREY DOUGLAS

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					SOFTWARE APPLICATIONS ON A COMPUTER		
06820460	4851987		150	01/17/1986	SYSTEM FOR REDUCING PROCESSOR POWER CONSUMPTION BY STOPPING PROCESSOR CLOCK SUPPLY IF A DESIRED EVENT DOES NOT OCCUR	COLEMAN,ERIC	DAY, MICHAEL N.
07225570	4907150		150	07/26/1988	APPARATUS AND METHOD FOR SUSPENDING AND RESUMING SOFTWARE APPLICATIONS ON A COMPUTER	CLARK,DAVID	DAY, MICHAEL N.
08755863	Not Issued		168	12/02/1996	SHARED LOOP AUDIO/VIDEO SERVER SYSTEM	KUPSTAS,TOD	DAY, MICHAEL N.
08770027	6016400		250	12/19/1996	PRELOADING SOFTWARE ONTO A COMPUTER SYSTEM	STAMBER,ERIC	DAY, MICHAEL N.
08961706	5996015		150	10/31/1997	METHOD OF DELIVERING SEAMLESS AND CONTINUOUS PRESENTATION OF MULTIMEDIA DATA FILES TO A TARGET DEVICE BY ASSEMBLING AND CONCATENATING MULTIMEDIA SEGMENTS IN MEMORY	VU,VIET	DAY, MICHAEL NORMA
10318541	7103748	20040117592	150	12/12/2002	MEMORY MANAGEMENT FOR REAL-TIME APPLICATIONS	SONG,JASMINE	DAY, MICHAEL NORMAN
10318594	7062612	20040117560	150	12/12/2002	UPDATING REMOTE LOCKED CACHE	ANDERSON,MATTHEW	DAY, MICHAEL NORMAN
10322127	6820143	20040117520	150	12/17/2002	ON-CHIP DATA TRANSFER IN MULTI-PROCESSOR SYSTEM	CHEN,ALAN	DAY, MICHAEL NORMAN
10366440	6961820	20040162946	150	02/13/2003	SYSTEM AND METHOD FOR IDENTIFYING AND ACCESSING STREAMING DATA IN A LOCKED PORTION OF A CACHE	MOAZZAMI,NASSER	DAY, MICHAEL NORMAN
10443727	7114042	20040236914	150	05/22/2003	METHOD TO PROVIDE ATOMIC UPDATE PRIMITIVES IN AN ASYMMETRIC HETEROGENEOUS MULTIPROCESSOR ENVIRONMENT	ELMORE,REBA	DAY, MICHAEL NORMAN
10448237	7200688	20040243738	150	05/29/2003	SYSTEM AND METHOD ASYNCHRONOUS DMA COMMAND COMPLETION NOTIFICATION BY ACCESSING REGISTER VIA ATTACHED PROCESSING UNIT TO DETERMINE PROGRESS OF DMA COMMAND	PEYTON,TAMMARA	DAY, MICHAEL NORMAN
10455169	6981072	20040249995	150	06/05/2003	MEMORY MANAGEMENT IN MULTIPROCESSOR SYSTEM	SHIN,CHRISTOPHER	DAY, MICHAEL NORMAN
10460423	7089373	20040255084	150	06/12/2003	SHADOW REGISTER TO ENHANCE LOCK ACQUISITION	PATEL,KAUSHIKKUMAR	DAY, MICHAEL NORMAN
10464882	7107363	20040260746	150	06/19/2003	MICROPROCESSOR HAVING BANDWIDTH MANAGEMENT FOR COMPUTING APPLICATIONS AND RELATED METHOD OF MANAGING BANDWIDTH ALLOCATION	KIM,HAROLD	DAY, MICHAEL NORMAN
10601374	Not Issued	20050021944	121	06/23/2003	Security architecture for system on chip	JOHNSON,CARLTON	DAY, MICHAEL NORMAN
10631537	7356713	20050028015	150	07/31/2003	METHOD AND APPARATUS FOR MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	BROWN,MICHAEL	DAY, MICHAEL NORMAN
10655365	7114035	20050055505	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT WITH SOFTWARE-GENERATED CLASS IDENTIFIERS	DOAN,DUC	DAY, MICHAEL NORMAN
10655367	7120748	20050055507	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT	DOAN,DUC	DAY, MICHAEL NORMAN
10670823	7523157	20050091473	150	09/25/2003	MANAGING A PLURALITY OF PROCESSORS AS DEVICES	JEAN GILLES,JUDE	DAY, MICHAEL NORMAN
10670825	7475257	20050071651	150	09/25/2003	SYSTEM AND METHOD FOR SELECTING AND USING A SIGNAL PROCESSOR IN A MULTIPROCESSOR SYSTEM TO OPERATE AS A SECURITY FOR ENCRYPTION/DECRYPTION OF DATA	PARTHASARATHY,PRAMILA	DAY, MICHAEL NORMAN
10670833	7389508	20050081201	150	09/25/2003	SYSTEM AND METHOD FOR GROUPING PROCESSORS AND ASSIGNING SHARED MEMORY SPACE TO A GROUP IN HETEROGENEOUS COMPUTER ENVIRONMENT	KAWSAR,ABDULLAH AL	DAY, MICHAEL NORMAN
10670835	7496917	20050071526	150	09/25/2003	VIRTUAL DEVICES USING A PLURALITY OF PROCESSORS	KAWSAR,ABDULLAH AL	DAY, MICHAEL NORMAN
10670838	7478390	20050081202	150	09/25/2003	TASK QUEUE MANAGEMENT OF VIRTUAL DEVICES USING A PLURALITY OF PROCESSORS	TO,JENNIFER	DAY, MICHAEL NORMAN
10670840	Not Issued	20050071578	168	09/25/2003	System and method for manipulating data with a plurality of processors	KROFCHECK,MICHAEL	DAY, MICHAEL NORMAN
10670841	7516456	20050081203	150	09/25/2003	ASYMMETRIC HETEROGENEOUS MULTI-THREADED OPERATING SYSTEM	WAI,ERIC	DAY, MICHAEL NORMAN
10670842	7415703	20050086655	150	09/25/2003	LOADING SOFTWARE ON A PLURALITY OF PROCESSORS	TRUONG,CAMQUY	DAY, MICHAEL NORMAN
10682386	7093080	20050080998	150	10/09/2003	METHOD AND APPARATUS FOR COHERENT MEMORY STRUCTURE OF HETEROGENEOUS PROCESSOR SYSTEMS	MOAZZAMI,NASSER	DAY, MICHAEL NORMAN
10697865	7363544	20050097398	150	10/30/2003	PROGRAM DEBUG METHOD AND APPARATUS	SCHELL,JOSEPH	DAY, MICHAEL NORMAN
10718936	Not Issued	20050111478	71	11/20/2003	Distributed control load shaping method and apparatus	PASIA,REDENTOR	DAY, MICHAEL NORMAN
10725129	Not Issued	20050120185	161	12/01/2003	Methods and apparatus for efficient multi-tasking	CAMPOS,YAIMA	DAY, MICHAEL NORMAN
10738720	7530068	20050138621	150	12/17/2003	METHOD OF RESOURCE ALLOCATION USING AN ACCESS CONTROL MECHANISM	TRUONG,CAMQUY	DAY, MICHAEL NORMAN
10759941	Not Issued	20050160229	161	01/16/2004	Method and apparatus for preloading translation buffers	KROFCHECK,MICHAEL	DAY, MICHAEL NORMAN
10891772	7243200	20060015652	150	07/15/2004	ESTABLISHING COMMAND ORDER IN AN OUT OF ORDER DMA COMMAND QUEUE	FARROKH,HASHEM	DAY, MICHAEL NORMAN
10891773	Not Issued	20060015876	93	07/15/2004	LIGHT WEIGHT CONTEXT SWITCHING	ARCOS,CAROLINE	DAY, MICHAEL NORMAN
10902474	Not Issued	20060026309	61	07/29/2004	Memory barriers primitives in an asymmetric heterogeneous multiprocessor environment	MAMO,ELIAS	DAY, MICHAEL NORMAN
10912479	7299371	20060031835	150	08/05/2004	HIERARCHICAL MANAGEMENT FOR MULTIPROCESSOR SYSTEM	DU,THUAN	DAY, MICHAEL NORMAN

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10912481	7299372	20060031836	150	08/05/2004	HIERARCHICAL MANAGEMENT FOR MULTIPROCESSOR SYSTEM WITH REAL-TIME ATTRIBUTES	DU,THUAN	DAY, MICHAEL NORMAN
10926592	7240137	20060047875	150	08/26/2004	SYSTEM AND METHOD FOR MESSAGE DELIVERY ACROSS A PLURALITY OF PROCESSORS	DALEY,CHRISTOPHER	DAY, MICHAEL NORMAN
10942432	7240182	20060095718	150	09/16/2004	SYSTEM AND METHOD FOR PROVIDING A PERSISTENT FUNCTION SERVER	COLEMAN,ERIC	DAY, MICHAEL NORMAN
10955093	7290112	20060069878	150	09/30/2004	SYSTEM AND METHOD FOR VIRTUALIZATION OF PROCESSOR RESOURCES	YU,JAE	DAY, MICHAEL NORMAN
10955184	Not Issued	20060070069	124	09/30/2004	Sharing resources between real-time and virtualizing operating systems	CHRZANOWSKI,MATTHEW	DAY, MICHAEL NORMAN
10965634	Not Issued	20060085791	41	10/14/2004	Method for broadcasting a condition to threads executing on a plurality of on-chip processors	TRUONG,CAMQUY	DAY, MICHAEL NORMAN
10976260	7290106	20060095668	150	10/28/2004	METHOD FOR PROCESSOR TO USE LOCKING CACHE AS PART OF SYSTEM MEMORY	SCHLIE,PAUL	DAY, MICHAEL NORMAN
10976263	7290107	20060095669	150	10/28/2004	DIRECT DEPOSIT USING LOCKING CACHE	SCHLIE,PAUL	DAY, MICHAEL NORMAN
10976264	Not Issued	20060095898	41	10/28/2004	Method for integrating multiple object files from heterogeneous architectures into a set of files	DAO,THUY	DAY, MICHAEL NORMAN
11029891	Not Issued	20060149861	61	01/05/2005	Methods and apparatus for list transfers using DMA transfers in a multi-processor system	SUN,MICHAEL	DAY, MICHAEL NORMAN
11204415	7421453	20070043746	150	08/16/2005	ASYNCHRONOUS LINKED DATA STRUCTURE TRAVERSAL	NG,AMY	DAY, MICHAEL NORMAN
11341702	Not Issued	20060259733	61	01/27/2006	Methods and apparatus for resource management in a logically partitioned processing environment	PATEL,NIMESH	DAY, MICHAEL NORMAN
11468317	Not Issued	20070016733	41	08/30/2006	Method to Provide Atomic Update Primitives in an Asymmetric Heterogeneous Multiprocessor Environment	ELMORE,REBA	DAY, MICHAEL NORMAN
11678141	Not Issued	20080209127	41	02/23/2007	System and method for efficient implementation of software-managed cache	SUN,MICHAEL	DAY, MICHAEL NORMAN
11695436	7546393	20070174509	150	04/02/2007	SYSTEM FOR ASYNCHRONOUS DMA COMMAND COMPLETION NOTIFICATION WHEREIN THE DMA COMMAND COMPRISING A TAG BELONGS TO A PLURALITY OF TAG GROUPS	PEYTON,TAMMARA	DAY, MICHAEL NORMAN
11745018	Not Issued	20080282064	30	05/07/2007	System and Method for Speculative Thread Assist in a Heterogeneous Processing Environment	ZHEN,WEI	DAY, MICHAEL NORMAN
11874513	Not Issued	20080040548	93	10/18/2007	METHOD FOR PROCESSOR TO USE LOCKING CACHE AS PART OF SYSTEM MEMORY	BATAILLE,PIERRE MICHE	DAY, MICHAEL NORMAN
11875407	Not Issued	20080040549	93	10/19/2007	DIRECT DEPOSIT USING LOCKING CACHE	BATAILLE,PIERRE MICHE	DAY, MICHAEL NORMAN
11959998	Not Issued	20080098264	41	12/19/2007	PROGRAM DEBUG METHOD AND APPARATUS	SCHELL,JOSEPH	DAY, MICHAEL NORMAN
12025649	Not Issued	20080126817	41	02/04/2008	METHOD AND APPARATUS FOR MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	BROWN,MICHAEL	DAY, MICHAEL NORMAN
12042254	Not Issued	20080155203	30	03/04/2008	Grouping processors and assigning shared memory space to a group in a heterogeneous computer environment	KAWSAR,ABDULLAH AL	DAY, MICHAEL NORMAN
12049179	Not Issued	20080168443	20	03/14/2008	Virtual Devices Using a Plurality of Processors	,	DAY, MICHAEL NORMAN
12049295	Not Issued	20080162834	30	03/15/2008	Task Queue Management of Virtual Devices Using a Plurality of Processors	TO,JENNIFER	DAY, MICHAEL NORMAN
12049317	Not Issued	20080163241	30	03/15/2008	Light Weight Task Switching When a Shared	AN,MENG AI	DAY, MICHAEL NORMAN

12249295	Not Issued	20080162834	30	03/15/2008	Task Queue Management of Virtual Devices Using a Plurality of Processors	TO,JENNIFER	DAY, MICHAEL NORMAN
12249317	Not Issued	20080163241	30	03/15/2008	Light Weight Task Switching When a Shared Memory Condition is Signaled	AN,MENG AI	DAY, MICHAEL NORMAN
12131348	Not Issued	20080235679	25	06/02/2008	Loading Software on a Plurality of Processors		DAY, MICHAEL NORMAN
12147540	Not Issued	20080263091	30	06/27/2008	Asynchronous Linked Data Structure Traversal	GAFFIN,JEFFREY	DAY, MICHAEL NORMAN
12176575	Not Issued	20080301695	25	07/19/2008	Managing a Plurality of Processors as Devices		DAY, MICHAEL NORMAN
08755874	5892913		150	12/02/1996	SYSTEM AND METHOD FOR DATASTREAMS EMPLOYING SHARED LOOP ARCHITECTURE MULTIMEDIA SUBSYSTEM CLUSTERS	TANNER,CHRISTOPHER	DAY, MICHAEL NORMAN
08958823	6032191		250	10/28/1997	DIRECT COUPLING FOR DATA TRANSFERS	MAUNG,ZARNI	DAY, MICHAEL NORMAN
08961468	5941951		150	10/31/1997	METHODS FOR REAL-TIME DETERMINISTIC DELIVERY OF MULTIMEDIA DATA IN A CLIENT/SERVER SYSTEM	HARRELL,ROBERT	DAY, MICHAEL NORMAN
08961704	5996025		150	10/31/1997	NETWORK TRANSPARENT ACCESS FRAMEWORK FOR MULTIMEDIA SERVING	HARRELL,ROBERT	DAY, MICHAEL NORMAN
11204424	Not Issued	20070043916	30	08/16/2005	Light weight task switching when a shared memory condition is signaled	AN,MENG AI	DAY, MICHAEL NORMAN

					DATA BETWEEN MEMORIES IF THERE IS SUFFICIENT DATA FOR EFFICIENT TRASMISSION DEPENDING ON READ WRITE POINTERS		
08687854	6061069		150	07/26/1996	APPARATUS AND METHOD OF PERFORMING SCREEN TO SCREEN BLITS IN A COLOR SLICED FRAME BUFFER ARCHITECTURE	PIZIALI,JEFFREY	JOHNS, CHARLES R.
09661280	6674841		150	09/14/2000	METHOD AND APPARATUS IN A DATA PROCESSING SYSTEM FOR AN ASYNCHRONOUS CONTENT SWITCHING MECHANISM	HOOSAIN,ALLAN	JOHNS, CHARLES RAY
09815558	6809734	20020135582	150	03/22/2001	RESOURCE DEDICATION SYSTEM AND METHOD FOR A COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	TUNG,KEE	JOHNS, CHARLES RAY
09816004	7233998	20020138637	150	03/22/2001	COMPUTER ARCHITECTURE AND SOFTWARE CELLS FOR BROADBAND NETWORKS	MANIWANG,JOSEPH	JOHNS, CHARLES RAY
09833348	Not Issued	20020158885	124	04/12/2001	Method and apparatus for generating gammacorrected antialiased lines	AMINI,JAVID	JOHNS, CHARLES RAY
09895888	6760819	20030005237	150	06/29/2001	SYMMETRIC MULTIPROCESSOR COHERENCE MECHANISM	PORTKA,GARY	JOHNS, CHARLES RAY
10242522	7003604	20040054830	150	09/12/2002	METHOD AND SYSTEM FOR AN IMPROVED DIFFERENTIAL FORM OF TRANSITIONAL CODING	STIGLIC,RYAN	JOHNS, CHARLES RAY
10242523	7209137	20040051715	150	09/12/2002	EFFICIENT TRIANGULAR SHAPED MESHES	NGUYEN,HAU	JOHNS, CHARLES RAY
10242525	7248696	20040052375	150	09/12/2002	DYNAMIC SYSTEM BUS ENCRYPTION USING IMPROVED DIFFERENTIAL TRANSITIONAL ENCODING	REVAK,CHRISTOPHER	JOHNS, CHARLES RAY
10318541	7103748	20040117592	150	12/12/2002	MEMORY MANAGEMENT FOR REAL-TIME APPLICATIONS	SONG,JASMINE	JOHNS, CHARLES RAY
10318594	7062612	20040117560	150	12/12/2002	UPDATING REMOTE LOCKED CACHE	ANDERSON,MATTHEW	JOHNS, CHARLES RAY
10322127	6820143	20040117520	150	12/17/2002	ON-CHIP DATA TRANSFER IN MULTI-PROCESSOR SYSTEM	CHEN,ALAN	JOHNS, CHARLES RAY
10346294	6931493	20040143706	150	01/16/2003	IMPLEMENTATION OF AN LRU AND MRU ALGORITHM IN A PARTITIONED CACHE	NGUYEN,HIEP	JOHNS, CHARLES RAY
10366440	6961820	20040162946	150	02/13/2003	SYSTEM AND METHOD FOR IDENTIFYING AND ACCESSING STREAMING DATA IN A LOCKED PORTION OF A CACHE	MOAZZAMI,NASSER	JOHNS, CHARLES RAY
10443727	7114042	20040236914	150	05/22/2003	METHOD TO PROVIDE ATOMIC UPDATE PRIMITIVES IN AN ASYMMETRIC HETEROGENEOUS MULTIPROCESSOR ENVIRONMENT	ELMORE,REBA	JOHNS, CHARLES RAY
10448237	7200688	20040243738	150	05/29/2003	SYSTEM AND METHOD ASYNCHRONOUS DMA COMMAND COMPLETION NOTIFICATION BY ACCESSING REGISTER VIA ATTACHED PROCESSING UNIT TO DETERMINE PROGRESS OF DMA COMMAND	PEYTON,TAMMARA	JOHNS, CHARLES RAY
10455169	6981072	20040249995	150	06/05/2003	MEMORY MANAGEMENT IN MULTIPROCESSOR SYSTEM	SHIN,CHRISTOPHER	JOHNS, CHARLES RAY
10464882	7107363	20040260746	150	06/19/2003	MICROPROCESSOR HAVING BANDWIDTH MANAGEMENT FOR COMPUTING APPLICATIONS AND RELATED METHOD OF MANAGING BANDWIDTH ALLOCATION	KIM,HAROLD	JOHNS, CHARLES RAY
10601374	Not Issued	20050021944	121	06/23/2003	Security architecture for system on chip	JOHNSON,CARLTON	JOHNS, CHARLES RAY
10607360	7174398	20040267767	150	06/26/2003	METHOD AND APPARATUS FOR IMPLEMENTING DATA MAPPING WITH SHUFFLE ALGORITHM	SUN,SCOTT	JOHNS, CHARLES RAY
10631537	7356713	20050028015	150	07/31/2003	METHOD AND APPARATUS FOR MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	BROWN,MICHAEL	JOHNS, CHARLES RAY
10655365	7114035	20050055505	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT WITH SOFTWARE-GENERATED CLASS IDENTIFIERS	DOAN,DUC	JOHNS, CHARLES RAY
10655370	7225277	20050055478	150	09/04/2003	PROXY DIRECT MEMORY ACCESS	MARTINEZ,DAVID	JOHNS, CHARLES RAY
10682386	7093080	20050080998	150	10/09/2003	METHOD AND APPARATUS FOR COHERENT MEMORY STRUCTURE OF HETEROGENEOUS PROCESSOR SYSTEMS	MOAZZAMI,NASSER	JOHNS, CHARLES RAY
10697897	7321958	20050097280	150	10/30/2003	SYSTEM AND METHOD FOR SHARING MEMORY BY HETEROGEN OUS PROCESSORS	BATAILLE,PIERRE MICHE	JOHNS, CHARLES RAY
10697903	Not Issued	20050097231	124	10/30/2003	System and method for a configurable interface controller	HASSAN,AURANGZEB	JOHNS, CHARLES RAY
10718302	Not Issued	20050111354	30	11/20/2003	Controlling bandwidth reservations method and apparatus	HAILE,FEBEN	JOHNS, CHARLES RAY
10718956	Not Issued	20050111478	71	11/20/2003	Distributed control load shaping method and apparatus	PASIA,REDENTOR	JOHNS, CHARLES RAY
10730952	7099975	20050125581	150	12/09/2003	IMPROVED METHOD OF RESOURCE ARBITRATION	DANG,KHANH	JOHNS, CHARLES RAY
10738720	7530068	20050138621	150	12/17/2003	METHOD OF RESOURCE ALLOCATION USING AN ACCESS CONTROL MECHANISM	TRUONG,CAMQUY	JOHNS, CHARLES RAY
10759939	7143246	20050160239	150	01/16/2004	METHOD FOR SUPPORTING IMPROVED BURST TRANSFERS ON A COHERENT BUS	KROFCHECK,MICHAEL	JOHNS, CHARLES RAY
10759941	Not Issued	20050160229	161	01/16/2004	Method and apparatus for preloading translation buffers	KROFCHECK,MICHAEL	JOHNS, CHARLES RAY
10809553	Not Issued	20050216610	124	03/25/2004	Method to provide cache management commands for a DMA controller	LEE,CHUN KUAN	JOHNS, CHARLES RAY
10809579	7321956	20050216672	150	03/25/2004	METHOD AND APPARATUS FOR DIRECTORY-BASED COHERENCE WITH DISTRIBUTED DIRECTORY MANAGEMENT UTILIZING PREFETCH CACHES	GU,SHAWN	JOHNS, CHARLES RAY
10809581	7363432	20050216675	150	03/25/2004	METHOD AND APPARATUS FOR DIRECTORY-BASED COHERENCE WITH DISTRIBUTED DIRECTORY MANAGEMENT	GU,SHAWN	JOHNS, CHARLES RAY
10891772	7243200	20060015652	150	07/15/2004	ESTABLISHING COMMAND ORDER IN AN OUT OF ORDER DMA COMMAND QUEUE	FARROKH,HASHEM	JOHNS, CHARLES RAY
10902474	Not Issued	20060026309	61	07/29/2004	Memory barriers primitives in an asymmetric heterogeneous multiprocessor environment	MAMO,ELIAS	JOHNS, CHARLES RAY
10912479	7299371	20060031835	150	08/05/2004	HIERARCHICAL MANAGEMENT FOR MULTIPROCESSOR SYSTEM	DU,THUAN	JOHNS, CHARLES RAY
10912481	7299372	20060031836	150	08/05/2004	HIERARCHICAL MANAGEMENT FOR MULTIPROCESSOR SYSTEM WITH REAL-TIME ATTRIBUTES	DU,THUAN	JOHNS, CHARLES RAY
10942422	Not Issued	20060059273	71	09/16/2004	Envelope packet architecture for broadband engine	TIV,BACKHEAN	JOHNS, CHARLES RAY
10982019	7102417	20060097757	150	11/05/2004	INTEGRATED CIRCUIT DIE INCLUDING A TEMPERATURE DETECTION CIRCUIT, AND SYSTEM AND METHODS FOR CALIBRATING THE TEMPERATURE DETECTION CIRCUIT	WELLS,KENNETH	JOHNS, CHARLES RAY
11029891	Not Issued	20060149861	61	01/05/2005	Methods and apparatus for list transfers using DMA transfers in a multi-processor system	SUN,MICHAEL	JOHNS, CHARLES RAY
11032194	Not Issued	20060155955	168	01/10/2005	SIMD-RISC processor module	PARTRIDGE,WILLIAM	JOHNS, CHARLES RAY
11050032	7389363	20060179168	150	02/03/2005	SYSTEM AND METHOD FOR FLEXIBLE MULTIPLE PROTOCOLS	BORROMEO,JUANITO	JOHNS, CHARLES RAY
11050038	Not Issued	20050138325	41	02/03/2005	Multi-chip module with third dimension interconnect	ENG,DAVID	JOHNS, CHARLES RAY

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11548829	Not Issued	20080091879	89	10/12/2006	Method and structure for interrupting L2 cache live-lock occurrences	DARE,RYAN	TRUONG, THUONG Q.
11548831	Not Issued	20080091866	61	10/12/2006	Maintaining forward progress in a shared L2 by detecting and breaking up requestor starvation	DANG,KHANH	TRUONG, THUONG Q.
11556346	7519780	20080109585	150	11/03/2006	SYSTEM AND METHOD FOR REDUCING STORE LATENCY IN SYMMETRICAL MULTIPROCESSOR SYSTEMS	NGUYEN,HIEP	TRUONG, THUONG Q
11561010	Not Issued	20080120523	30	11/17/2006	EFFICIENT AND FLEXIBLE TRACE TRIGGER HANDLING FOR NON-CONCURRENT EVENTS	CHU,GABRIEL	TRUONG, THUONG Q.
12106483	Not Issued	20080244200	25	04/21/2008	System for Communicating Command Parameters Between a Processor and a Memory Flow Controller	,	TRUONG, THUONG Q
12129114	Not Issued	20080288757	25	05/29/2008	Communicating Instructions and Data Between a Processor and External Devices	,	TRUONG, THUONG Q
09895888	6760819	20030005237	150	06/29/2001	SYMMETRIC MULTIPROCESSOR COHERENCE MECHANISM	PORTKA,GARY	TRUONG, THUONG QUANG
10318541	7103748	20040117592	150	12/12/2002	MEMORY MANAGEMENT FOR REAL-TIME APPLICATIONS	SONG,JASMINE	TRUONG, THUONG QUANG
10318594	7062612	20040117560	150	12/12/2002	UPDATING REMOTE LOCKED CACHE	ANDERSON,MATTHEW	TRUONG, THUONG QUANG
10322127	6820143	20040117520	150	12/17/2002	ON-CHIP DATA TRANSFER IN MULTI-PROCESSOR SYSTEM	CHEN,ALAN	TRUONG, THUONG QUANG
10366440	6961820	20040162946	150	02/13/2003	SYSTEM AND METHOD FOR IDENTIFYING AND ACCESSING STREAMING DATA IN A LOCKED PORTION OF A CACHE	MOAZZAMI,NASSER	TRUONG, THUONG QUANG
10443727	7114042	20040236914	150	05/22/2003	METHOD TO PROVIDE ATOMIC UPDATE PRIMITIVES IN AN ASYMMETRIC HETEROGENEOUS MULTIPROCESSOR ENVIRONMENT	ELMORE,REBA	TRUONG, THUONG QUANG
10448237	7200688	20040243738	150	05/29/2003	SYSTEM AND METHOD MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	PEYTON,TAMMARA	TRUONG, THUONG QUANG
10655365	7114035	20050055505	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT WITH SOFTWARE-GENERATED CLASS IDENTIFIERS	DOAN,DUC	TRUONG, THUONG QUANG
10655366	7055004	20050055506	150	09/04/2003	PSEUDO-LRU FOR A LOCKING CACHE	PEIKARI,BEHZAD	TRUONG, THUONG QUANG
10655367	7120748	20050055507	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT	DOAN,DUC	TRUONG, THUONG QUANG
10655370	7225277	20050055478	150	09/04/2003	PROXY DIRECT MEMORY ACCESS	MARTINEZ,DAVID	TRUONG, THUONG QUANG
10682386	7093080	20050080998	150	10/09/2003	METHOD AND APPARATUS FOR COHERENT MEMORY STRUCTURE OF HETEROGENEOUS PROCESSOR SYSTEMS	MOAZZAMI,NASSER	TRUONG, THUONG QUANG
10718936	Not Issued	20050111478	71	11/20/2003	Distributed control load shaping method and apparatus	PASIA,REDENTOR	TRUONG, THUONG QUANG
10809553	Not Issued	20050216610	124	03/25/2004	Method to provide cache management commands for a DMA controller	LEE,CHUN KUAN	TRUONG, THUONG QUANG
10860426	7353341	20050273563	150	06/03/2004	SYSTEM AND METHOD FOR CANCELING WRITE BACK OPERATION DURING SIMULTANEOUS SNOOP PUSH OR SNOOP KILL OPERATION IN WRITE BACK CACHES	CAMPOS,YAIMA	TRUONG, THUONG QUANG
10875953	Not Issued	20050289300	168	06/24/2004	Disable write back on atomic reserved line in a small cache system	WANG,VICTOR	TRUONG, THUONG QUANG
10891796	Not Issued	20060015689	121	07/15/2004	Implementation and management of moveable buffers in cache system	RUIZ,ARACELIS	TRUONG, THUONG QUANG
10916969	7472229	20060036814	150	08/12/2004	BUS CONTROLLER INITIATED WRITE-THROUGH MECHANISM	CHOE,YONG	TRUONG, THUONG QUANG
11121121	Not Issued	20060253662	168	05/03/2005	Retry cancellation mechanism to enhance system performance	SCHNEE,HAL	TRUONG, THUONG QUANG
11284257	Not Issued	20070118837	30	11/21/2005	Method and apparatus for preventing livelocks in processor selection of load requests	AN,MENG AI	TRUONG, THUONG QUANG
11468317	Not Issued	20070016733	41	08/30/2006	Method to Provide Atomic Update Primitives in an Asymmetric Heterogeneous Multiprocessor Environment	ELMORE,REBA	TRUONG, THUONG QUANG
11695436	7546393	20070174509	150	04/02/2007	SYSTEM FOR ASYNCHRONOUS DMA COMMAND COMPLETION NOTIFICATION WHEREIN THE DMA COMMAND COMPRISING A TAG BELONGS TO A PLURALITY OF TAG GROUPS	PEYTON,TAMMARA	TRUONG, THUONG QUANG
11828439	Not Issued	20090031085	30	07/26/2007	Directory for Multi-Node Coherent Bus	BRAGDON,REGINALD	TRUONG, THUONG QUANG
11828448	Not Issued	20090031086	30	07/26/2007	Directory For Multi-Node Coherent Bus	BRAGDON,REGINALD	TRUONG, THUONG QUANG
11930393	Not Issued	20090113138	30	10/31/2007	Combined Response Cancellation for Load Command	BRAGDON,REGINALD	TRUONG, THUONG QUANG
11959652	Not Issued	0	30	12/19/2007	SYSTEM AND METHOD FOR OPTIMIZING NEIGHBORING CACHE USAGE IN A MULTIPROCESSOR ENVIRONMENT	BRAGDON,REGINALD	TRUONG, THUONG QUANG
11959793	Not Issued	0	30	12/19/2007	System and Method for Cache Coherency In A Multiprocessor System	BRAGDON,REGINALD	TRUONG, THUONG QUANG

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11959804	Not Issued	0	30	12/19/2007	System and Method for Cache Line Replacement Selection in a Multiprocessor Environment	BRAGDON REGINALD	TRUONG, THUONG QUANG
12025649	Not Issued	20090199017	41	02/04/2008	METHOD AND APPARATUS FOR MANAGING THE POWER CONSUMPTION OF A DATA PROCESSING SYSTEM	BROWN, MICHAEL	TRUONG, THUONG QUANG
12273516	Not Issued	20090077322	25	11/19/2008	Bus Controller Initiated Write-Through Mechanism with Hardware Automatically Generated Clean Command		TRUONG, THUONG QUANG

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Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name	Inventor Name
06207796	4323702		150	11/17/1980	PROCESS FOR RECOVERING A CARBOXYLIC ACID	SIEGEL,ALAN	YAMAZAKI, TAKESHI
06573315	4636474		150	01/24/1984	TOILET APPARATUS	FLANIGAN,ALLEN	YAMAZAKI, TAKESHI
07099854	4806444		150	09/22/1987	METHOD AND APPARATUS FOR COATING WEBS	BECK,SHRIVE	YAMAZAKI, TAKESHI
07247259	Not Issued		166	09/21/1988	MOTORCYCLE PROVIDED WITH TRUNKS	CULBRETH,ERIC	YAMAZAKI, TAKESHI
07449417	5025883		150	12/15/1989	MOTORCYCLE PROVIDED WITH TRUNKS	CULBRETH,ERIC	YAMAZAKI, TAKESHI
07458261	5129163		150	12/28/1989	MULTISTAGE TENTER	BENNETT,HENRY	YAMAZAKI, TAKESHI
07556363	5233444		150	07/24/1990	FOCUS ERROR DETECTING APPARATUS	PARSONS,DAVID	YAMAZAKI, TAKESHI
07736342	5188788		150	07/26/1991	PROCESS FOR PRODUCING CELLULOSE TRIACETATE FILMS	VARGOT,MATHIEU	YAMAZAKI, TAKESHI
07927410	5438415		150	09/22/1992	ELLIPSOMETER AND METHOD OF CONTROLLING COATING THICKNESS THEREWITH	PHAM,HOA	YAMAZAKI, TAKESHI
08067804	5397724		150	05/27/1993	METHOD OF MAKING A NONVOLATILE MEMORY DEVICE HAVING A SIDEWALL INSULATING FILM DOPED WITH PHOSPHORUS	CHAUDHARI,CHANDRA	YAMAZAKI, TAKESHI
08224009	5406543		150	04/06/1994	OPTICAL HEAD WITH SEMICONDUCTOR LASER	NGUYEN,TAN	YAMAZAKI, TAKESHI
08333481	Not Issued		161	11/02/1994	OPTICAL SEMICONDUCTOR DEVICE	TRAN,MINH LOAN	YAMAZAKI, TAKESHI
08336782	5555232		150	11/09/1994	OPTICAL HEAD WITH BUILT-IN SEMICONDUCTOR LASER, OBJECTIVE LENS AND WAVELENGTH PLATE	CHU,KIM KWOK	YAMAZAKI, TAKESHI
08337190	5608695		150	11/07/1994	OPTICAL PICK-UP APPARATUS WITH TRACKING ERROR DETECTION BY DETECTION OF AMOUNT OF LIGHT IN FAR FIELD	TRAN,THANG	YAMAZAKI, TAKESHI
08341087	5648946		150	11/17/1994	OPTICAL PICK-UP APPARATUS WITH HOLOGRAPHIC OPICAL ELEMENT TO DIFFRACT BOTH FORWARD AND RETURN LIGHT BEAMS	TRAN,THANG	YAMAZAKI, TAKESHI
08378976	Not Issued		166	01/27/1995	NONVOLATILE MEMORY DEVICE AND PROCESS FOR PRODUCTION OF THE SAME	NGO,NGAN	YAMAZAKI, TAKESHI
08400757	5621714		150	03/08/1995	OPTICAL PICK-UP APPARATUS HAVING HOLOGRAM AND BEAM SPLITTER WITH BIREFRINGENT MEMBER AND POLARIZING FILM	CHU,KIM KWOK	YAMAZAKI, TAKESHI
08447008	5523248		150	05/22/1995	METHOD FOR MANUFACTURING MOS TRANSISTORS WITH HIGH BREAKDOWN VOLTAGE	PICARDAT,KEVIN	YAMAZAKI, TAKESHI
08457760	5705880		150	06/01/1995	PIEZOELECTRIC DEVICE	DOUGHERTY,THOMAS	YAMAZAKI, TAKESHI
08458900	Not Issued		161	06/02/1995	PIEZOELECTRIC DEVICE PROVIDED WITH INTERNAL TERMINALS	BUDD,MARK	YAMAZAKI, TAKESHI
08465056	5607209		250	06/05/1995	BRAKING APPARATUS AND BRAKING METHOD FOR VEHICLE	BALLATO,JOSIE	YAMAZAKI, TAKESHI
08497447	5649141		150	06/30/1995	MULTIPROCESSOR SYSTEM FOR LOCALLY MANAGING ADDRESS TRANSLATION TABLE	CHOW,CHRISTOPHER	YAMAZAKI, TAKESHI
08620353	Not Issued		166	03/22/1996	PHOTO FILM TAKE-UP SPOOL AND SHAFT	LEE,EDDIE	YAMAZAKI, TAKESHI
08677913	5614748		150	07/10/1996	NONVOLATILE MEMORY DEVICE AND PROCESS FOR PRODUCTION OF THE SAME	NGO,NGAN	YAMAZAKI, TAKESHI
08692346	5923855		250	08/05/1996	MULTI-PROCESSOR SYSTEM AND METHOD FOR SYNCHRONIZING AMONG PROCESSORS WITH CACHE MEMORY HAVING RESET STATE, INVALID STATE, AND VALID STATE	DINH,DUNG	YAMAZAKI, TAKESHI
08838040	5911426		250	04/22/1997	FRONT WHEEL SUSPENSION FOR SMALL SIZE MOTOR VEHICLE	DICKSON,PAUL	YAMAZAKI, TAKESHI
08878558	5938231		150	06/19/1997	AIR BAG DEVICE FOR A TWO-WHEELED MOTOR VEHICLE	RICE,KENNETH	YAMAZAKI, TAKESHI
08892437	Not Issued		161	07/14/1997	PIEZOELECTRIC RESONATOR	DOUGHERTY,THOMAS	YAMAZAKI, TAKESHI
08934767	5838998		150	09/22/1997	PHOTO FILM TAKE-UP SPOOL AND SHAFT	LEE,EDDIE	YAMAZAKI, TAKESHI
08963984	6546129		150	11/04/1997	IMAGE PROCESSING APPARATUS, METHOD, AND MEDIUM FOR ADDING IDENTIFICATION INFORMATION	CHEN,WENPENG	YAMAZAKI, TAKESHI
09010618	6879372		150	01/22/1998	IMAGE PROCESSING APPARATUS AND METHOD THEREOF	KIM,PETER	YAMAZAKI, TAKESHI
09065998	6028390		150	04/24/1998	PIEZOELECTRIC RESONATOR AND ELECTRONIC COMPONENT INCLUDING SAME	DOUGHERTY,THOMAS	YAMAZAKI, TAKESHI
09069116	6035520		150	04/29/1998	APPARATUS FOR MANUFACTURING LENS-FITTED PHOTO FILM UNIT	BRYANT,DAVID	YAMAZAKI, TAKESHI
09079870	6154914		150	05/15/1998	COSMETIC APPLICATOR AND PROCESS FOR PRODUCING THE SAME	MCNEIL,JENNIFER	YAMAZAKI, TAKESHI
09120898	6091180		150	07/22/1998	PIEZOELECTRIC RESONATOR AND ELECTRONIC COMPONENT USING THE SAME	DOUGHERTY,THOMAS	YAMAZAKI, TAKESHI
09148404	6195506		150	09/04/1998	PHOTO FILM TAKE-UP SPOOL AND SHAFT	GRAY,DAVID	YAMAZAKI, TAKESHI
09148405	Not Issued		161	09/04/1998	PHOTO FILM TAKE-UP SPOOL AND SHAFT	GRAY,DAVID	YAMAZAKI, TAKESHI
09150091	6483939	20010040996	150	09/09/1998	IMAGE PROCESSING	PATEL,KANJIBHAI	YAMAZAKI, TAKESHI
09159713	6054793		150	09/24/1998	PIEZOELECTRIC RESONATOR METHOD FOR ADJUSTING FREQUENCY OF PIEZOELECTRIC RESONATOR AND COMMUNICATION APPARATUS INCLUDING PIEZOELECTRIC RESONATOR	DOUGHERTY,THOMAS	YAMAZAKI, TAKESHI
09159906	6223406		150	09/24/1998	METHOD OF MANUFACTURING PIEZOELECTRIC RESONATOR	SMITH,SEAN	YAMAZAKI, TAKESHI
09160656	6114796		150	09/25/1998	PIEZOELECTRIC RESONATOR, METHOD FOR ADJUSTING FREQUENCY OF PIEZOELECTRIC RESONATOR AND COMMUNICATION APPARATUS INCLUDING SAME	MEDLEY,PETER	YAMAZAKI, TAKESHI
09325342	6285279		150	06/04/1999	INFORMATION DEVICE	SWARTHOUT,BRENT	YAMAZAKI, TAKESHI
09325543	6246339		150	06/04/1999	MOVABLE BODY DETECTING NOTIFYING SYSTEM	NGUYEN,HUNG	YAMAZAKI, TAKESHI
09411839	6567562		150	10/04/1999	ENCODING APPARATUS AND METHOD	SHERALI,ISHRAT	YAMAZAKI, TAKESHI
09412431	6711295	20030118242	150	10/05/1999	ENCODING APPARATUS AND METHOD, AND STORAGE MEDIUM	DO,ANH	YAMAZAKI, TAKESHI
09412432	6549676		150	10/05/1999	ENCODING DEVICE	SHERALI,ISHRAT	YAMAZAKI, TAKESHI
09413022	6560365		150	10/06/1999	DECODING APPARATUS AND METHOD	SHERALI,ISHRAT	YAMAZAKI, TAKESHI
09456297	6303844		150	12/08/1999	METHOD OF DECONTAMINATING MEDIUM CONTAINING POLYCHLORINATED BIPHENYLS OR DIOXINS	NAVE,EILEEN	YAMAZAKI, TAKESHI
09464089	6385838		150	12/16/1999	METHOD FOR MANUFACTURING LENS-FITTED PHOTO FILM UNIT	JIMENEZ,MARC	YAMAZAKI, TAKESHI
09536974	6215228		150	03/28/2000	Stacked piezoelectric resonator, characteristics-adjusting method thereof, and ladder-type filter including the same	ADDISON,KAREN	YAMAZAKI, TAKESHI

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09537591	6376970		150	03/28/2000	Piezoelectric resonator supporting structure and a piezoelectric component including the same	DOUGHERTY,THOMAS	YAMAZAKI, TAKESHI
09713235	7006257		150	11/16/2000	IMAGE PROCESSING APPARATUS, IMAGE PROCESSING METHOD, AND STORAGE MEDIUM	GRANT II,JEROME	YAMAZAKI, TAKESHI
09736585	6779049	20020078308	150	12/14/2000	SYMMETRIC MULTI-PROCESSING SYSTEM WITH ATTACHED PROCESSING UNITS BEING ABLE TO ACCESS A SHARED MEMORY WITHOUT BEING STRUCTURALLY CONFIGURED WITH AN ADDRESS TRANSLATION MECHANISM	PEIKARI,BEHZAD	YAMAZAKI, TAKESHI
09768548	6679429	20010015379	150	01/24/2001	BAR CODE SCANNING DEVICE	EVANS,FANNIE	YAMAZAKI, TAKESHI
09778932	6707465	20010012019	150	02/08/2001	DATA PROCESSING APPARATUS AND METHOD, AND STORAGE MEDIUM	SAJOUS,WESNER	YAMAZAKI, TAKESHI
09797885	6768819	20010031094	150	03/05/2001	IMAGE PROCESSING APPARATUS AND METHOD, AND STORAGE MEDIUM USED THEREWITH	DANG,DUY	YAMAZAKI, TAKESHI
09815554	6826662	20020138707	150	03/22/2001	SYSTEM AND METHOD FOR DATA SYNCHRONIZATION FOR A COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	NGUYEN,HIEP	YAMAZAKI, TAKESHI
09815558	6809734	20020135582	150	03/22/2001	RESOURCE DEDICATION SYSTEM AND METHOD FOR A COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	TUNG,KEE	YAMAZAKI, TAKESHI
09816004	7233998	20020138637	150	03/22/2001	COMPUTER ARCHITECTURE AND SOFTWARE CELLS FOR BROADBAND NETWORKS	MANIWANG,JOSEPH	YAMAZAKI, TAKESHI
09816020	6526491	20020138701	150	03/22/2001	MEMORY PROTECTION SYSTEM AND METHOD FOR COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	NGUYEN,HIEP	YAMAZAKI, TAKESHI
09816752	7093104	20020156993	150	03/22/2001	PROCESSING MODULES FOR COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	CHANG,JUNGWON	YAMAZAKI, TAKESHI
09833553	6433705	20010013834	150	04/13/2001	MOVABLE BODY DETECTING/NOTIFYING SYSTEM	NGUYEN,HUNG	YAMAZAKI, TAKESHI
09861733	6720712	20020011763	150	05/21/2001	PIEZOELECTRIC RESONANT COMPONENT	BUDD,MARK	YAMAZAKI, TAKESHI
09895033	6479917	20020005313	150	06/29/2001	MOTOR BRUSH STRUCTURE AND MOTOR-DRIVEN POWER STEERING DEVICE USING THE SAME MOTOR	LAM,THANH	YAMAZAKI, TAKESHI
10059325	7204355	20020100651	150	01/31/2002	SHOCK ABSORBING STRUCTURE OF TWO-WHEELED VEHICLE	TORRES WILLIAMS,MELANIE	YAMAZAKI, TAKESHI
10080704	6696975	20020080020	150	02/25/2002	MOVABLE BODY DETECTING/NOTIFYING SYSTEM	NGUYEN,HUNG	YAMAZAKI, TAKESHI
10095211	6919896	20030169269	150	03/11/2002	SYSTEM AND METHOD OF OPTIMIZING GRAPHICS PROCESSING	TRAN,TAM	YAMAZAKI, TAKESHI
10194353	6702184	20040007622	150	07/12/2002	COLLECTION OPTICS FOR LOW PROFILE SINGLE LINE SCANNING ASSEMBLY IN COMPACT BAR CODE READERS	KOYAMA,KUMIKO	YAMAZAKI, TAKESHI
10218594	6718058	20020191840	150	08/15/2002	PICTURE GENERATING APPARATUS	PATEL,KANJIBHAI	YAMAZAKI, TAKESHI
10277943	6980829	20030083098	150	10/23/2002	PORTABLE TERMINAL SYSTEM AND OPERATION METHOD THEREOF	PHUONG,DAI	YAMAZAKI, TAKESHI
10371322	7139882	20030229765	150	02/21/2003	MEMORY PROTECTION SYSTEM AND METHOD FOR COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	NGUYEN,HIEP	YAMAZAKI, TAKESHI
10392961	6793033	20030230886	150	03/21/2003	COMPACT VEHICLE	FLEMING,FAYE	YAMAZAKI, TAKESHI
10395397	7029029	20030218317	150	03/25/2003	AIRBAG APPARATUS	FREEDMAN,LAURA	YAMAZAKI, TAKESHI
10417102	7158662	20030198381	150	04/17/2003	IMAGE COMPRESSION METHOD AND APPARATUS, AND IMAGE CODING METHOD AND APPARATUS	HUNG,YUBIN	YAMAZAKI, TAKESHI
10417166	6932379	20040026904	150	04/17/2003	AIR BAG SYSTEM IN SCOOTER TYPE VEHICLE	ILAN,RUTH	YAMAZAKI, TAKESHI
10419136	7032923	20040007855	150	04/21/2003	AIR BAG APPARATUS FOR A SMALL SIZE VEHICLE	TO,TOAN	YAMAZAKI, TAKESHI
10418237	7200688	20040243738	150	05/29/2003	SYSTEM AND METHOD ASYNCHRONOUS DMA COMMAND COMPLETION NOTIFICATION BY ACCESSING REGISTER VIA ATTACHED PROCESSING UNIT TO DETERMINE PROGRESS OF DMA COMMAND	PEYTON,TAMMARA	YAMAZAKI, TAKESHI
10464882	7107363	20040260746	150	06/19/2003	MICROPROCESSOR HAVING BANDWIDTH MANAGEMENT FOR COMPUTING APPLICATIONS AND RELATED METHOD OF MANAGING BANDWIDTH ALLOCATION	KIM,HAROLD	YAMAZAKI, TAKESHI
10628452	7343084	20040022521	150	07/29/2003	IMAGE PROCESSING APPARATUS AND METHOD, COMPUTER PROGRAM, AND COMPUTER READABLE STORAGE MEDIUM	CHEVALIER,ROBERT	YAMAZAKI, TAKESHI
10631541	7512722	20050027902	150	07/31/2003	A METHOD FOR COMPLETING A PLURALITY OF CHAINED LIST DMA COMMANDS THAT INCLUDE A FENCED LIST DMA COMMAND ELEMENT	PATEL,NIKETA	YAMAZAKI, TAKESHI
10631542	7203811	20050027903	150	07/31/2003	NON-FENCED LIST DMA COMMAND MECHANISM	PATEL,NIKETA	YAMAZAKI, TAKESHI
10655365	7114035	20050055505	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT WITH SOFTWARE-GENERATED CLASS IDENTIFIERS	DOAN,DUC	YAMAZAKI, TAKESHI
10655367	7120748	20050055507	150	09/04/2003	SOFTWARE-CONTROLLED CACHE SET MANAGEMENT	DOAN,DUC	YAMAZAKI, TAKESHI
10655370	7225277	20050055476	150	09/04/2003	PROXY DIRECT MEMORY ACCESS	MARTINEZ,DAVID	YAMAZAKI, TAKESHI
10667914	7008039	20040061737	150	09/22/2003	WIPER FOR CLEANING JETTING HEAD AND LIQUID JETTING APPARATUS EQUIPPED THEREWITH	HSIEH,SHIH WEN	YAMAZAKI, TAKESHI
10718936	Not Issued	20050111478	71	11/20/2003	Distributed control load shaping method and apparatus	PASIA,REDENTOR	YAMAZAKI, TAKESHI
10725129	Not Issued	20050120185	161	12/01/2003	Methods and apparatus for efficient multi-tasking	CAMPOS,YAIMA	YAMAZAKI, TAKESHI
10730952	7099975	20050125581	150	12/09/2003	IMPROVED METHOD OF RESOURCE ARBITRATION	DANG,KHANH	YAMAZAKI, TAKESHI
10858844	Not Issued	20050144594	161	06/02/2004	System providing centralized management of a plurality of nodes on a network	NICKERSON,JEFFREY	YAMAZAKI, TAKESHI
10858854	Not Issued	20050144467	161	06/02/2004	Unauthorized access control apparatus between firewall and router	DADA,BEEMNET	YAMAZAKI, TAKESHI
10863759	7104628	20040252157	150	06/07/2004	INK JET PRINTER	HSIEH,SHIH WEN	YAMAZAKI, TAKESHI
10891772	7243200	20060015652	150	07/15/2004	ESTABLISHING COMMAND ORDER IN AN OUT OF ORDER DMA COMMAND QUEUE	FARROKH,HASHEM	YAMAZAKI, TAKESHI
10902473	Not Issued	20060026308	41	07/29/2004	DMAC issue mechanism via streaming ID method	MAMO,ELIAS	YAMAZAKI, TAKESHI
10902474	Not Issued	20060026309	61	07/29/2004	Memory barriers primitives in an asymmetric heterogeneous multiprocessor environment	MAMO,ELIAS	YAMAZAKI, TAKESHI
10959635	7231500	20050120187	150	10/05/2004	EXTERNAL DATA INTERFACE IN A COMPUTER ARCHITECTURE FOR BROADBAND NETWORKS	NGUYEN,HIEP	YAMAZAKI, TAKESHI
10959700	7516334	20050120254	150	10/05/2004	POWER MANAGEMENT FOR PROCESSING MODULES	CHANG,ERIC	YAMAZAKI, TAKESHI
10967362	Not Issued	20050081213	30	10/18/2004	System and method for data synchronization for a computer architecture for broadband networks	TRUONG,CAMQUY	YAMAZAKI, TAKESHI
10967363	Not Issued	20050078117	71	10/18/2004	System and method for data synchronization for a computer architecture for broadband networks	HSU,JONI	YAMAZAKI, TAKESHI
10967433	Not Issued	20050081209	30	10/18/2004	System and method for data synchronization for a computer architecture for broadband networks	TRUONG,CAMQUY	YAMAZAKI, TAKESHI
10967579	7457939	20050097302	150	10/18/2004	PROCESSING SYSTEM WITH DEDICATED LOCAL MEMORIES AND BUSY IDENTIFICATION	DOLLINGER,TONIA	YAMAZAKI, TAKESHI

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11011784	Not Issued	20060129786	71	12/14/2004	Methods and apparatus for address translation from an external device to a memory of a processor	PARIKH,KALPIT	YAMAZAKI, TAKESHI
11014226	Not Issued	20060126770	161	12/15/2004	Methods and apparatus for providing an asynchronous boundary between internal busses in a multi-processor device	NEFF,MICHAEL	YAMAZAKI, TAKESHI
11029891	Not Issued	20060149861	61	01/05/2005	Methods and apparatus for list transfers using DMA transfers in a multi-processor system	SUN,MICHAEL	YAMAZAKI, TAKESHI
11034086	7364254	20050156983	150	01/12/2005	INKJET PRINTER AND INKJET PRINTER CONTROLLING METHOD	FIDLER,SHELBY	YAMAZAKI, TAKESHI
11053487	Not Issued	20060179275	161	02/08/2005	METHODS AND APPARATUS FOR PROCESSING INSTRUCTIONS IN A MULTI-PROCESSOR SYSTEM USING MINIMAL SIZED INSTRUCTION BUFFER	VICARY,KEITH	YAMAZAKI, TAKESHI
11053689	Not Issued	20060179255	93	02/08/2005	METHODS AND APPARATUS FOR SYNCHRONIZING DATA ACCESS TO A LOCAL MEMORY IN A MULTI-PROCESSOR SYSTEM	AYASH,MARWAN	YAMAZAKI, TAKESHI
11077330	Not Issued	20060206657	124	03/10/2005	Single port/multiple ring implementation of a hybrid crossbar partially non-blocking data switch	VU,TRISHA	YAMAZAKI, TAKESHI
11098621	Not Issued	20060251070	71	04/04/2005	Methods and apparatus for facilitating coherency management in distributed multi-processor system	GIARDINO JR,MARK	YAMAZAKI, TAKESHI
11101091	7290849	20050231565	150	04/07/2005	INK JET PRINTER	VO,ANH	YAMAZAKI, TAKESHI
11108959	7302554	20050251648	150	04/19/2005	METHODS AND APPARATUS FOR MULTI-PROCESSOR PIPELINE PARALLELISM	LAI,VINCENT	YAMAZAKI, TAKESHI
11108971	7505457	20050238052	150	04/19/2005	METHOD AND APPARATUS FOR PROVIDING AN INTERCONNECTION NETWORK FUNCTION	HO,DUC	YAMAZAKI, TAKESHI
11110307	7383427	20050251655	150	04/20/2005	MULTI-SCALAR EXTENSION FOR SIMD INSTRUCTION SET PROCESSORS	PETRANEK,JACOB	YAMAZAKI, TAKESHI
11110492	Not Issued	20050251649	61	04/20/2005	Methods and apparatus for address map optimization on a multi-scalar extension	PETRANEK,JACOB	YAMAZAKI, TAKESHI
11110552	7401316	20050251762	150	04/20/2005	METHODS AND APPARATUS FOR SYNTHESIZABLE PIPELINE CONTROL	MEMULA,SURESH	YAMAZAKI, TAKESHI
11141700	Not Issued	20050275658	71	05/31/2005	Information processing apparatus with a cache memory and information processing method	THAI,TUAN	YAMAZAKI, TAKESHI
11153212	7364310	20050275813	150	06/15/2005	IMAGE PROJECTION SYSTEM	DOWLING,WILLIAM	YAMAZAKI, TAKESHI
11158687	Not Issued	20060174012	61	06/22/2005	Relay method, relay apparatus, and computer product	WILGOR,BRANDON	YAMAZAKI, TAKESHI
11195580	7398482	20070052562	150	07/28/2005	MODULAR DESIGN METHOD AND APPARATUS	DINH,PAUL	YAMAZAKI, TAKESHI
11221189	7535902	20060221973	150	09/07/2005	TRAFFIC DISTRIBUTION DEVICE, TRAFFIC DISTRIBUTION METHOD AND PACKET RELAY METHOD	SAM,PHIRIN	YAMAZAKI, TAKESHI
11250246	Not Issued	20060092957	71	10/14/2005	Methods and apparatus for supporting multiple configurations in a multi-processor system	NAM,HYUN	YAMAZAKI, TAKESHI
11251796	7436551	20060033942	150	10/18/2005	IMAGE PROCESSING APPARATUS, IMAGE PROCESSING METHOD, AND STORAGE MEDIUM	GRANT II,JEROME	YAMAZAKI, TAKESHI
11303330	Not Issued	20060092216	161	12/15/2005	Wipes for cleaning jetting head and liquid jetting apparatus equipped therewith	HSIEH,SHIH WEN	YAMAZAKI, TAKESHI
11341702	Not Issued	20060259733	61	01/27/2006	Methods and apparatus for resource management in a logically partitioned processing environment	PATEL,NIMESH	YAMAZAKI, TAKESHI
11370822	7497548	20060209121	150	03/08/2006	INKJET RECORDING APPARATUS	HSIEH,SHIH WEN	YAMAZAKI, TAKESHI
11431548	7354313	20060270283	150	05/11/2006	PHOTOELECTRIC COMBINED CONNECTOR	LEON,EDWIN	YAMAZAKI, TAKESHI
11432107	7461240	20060277437	150	05/11/2006	COMMAND EXECUTION CONTROLLING APPARATUS, COMMAND EXECUTION INSTRUCTING APPARATUS AND COMMAND EXECUTION CONTROLLING METHOD	COLEMAN,ERIC	YAMAZAKI, TAKESHI
11514202	Not Issued	20070067579	93	09/01/2006	SHARED MEMORY DEVICE	SPITTLE,MATTHEW	YAMAZAKI, TAKESHI
11547923	7368840	20070210658	150	10/05/2006	BRUSHLESS MOTOR	LAM,THANH	YAMAZAKI, TAKESHI
11578400	Not Issued	20070205679	161	10/12/2006	Brushless Motor	NGUYEN,HANH	YAMAZAKI, TAKESHI
11651417	Not Issued	20070168594	95	01/09/2007	SIGNAL TRANSMISSION METHOD, BRIDGE UNIT, AND INFORMATION PROCESSING APPARATUS	DALEY,CHRISTOPHER	YAMAZAKI, TAKESHI
11686083	7444435	20070174508	150	03/14/2007	NON-FENCED LIST DMA COMMAND MECHANISM	PATEL,NIKETA	YAMAZAKI, TAKESHI
11695436	7546393	20070174509	150	04/02/2007	SYSTEM FOR ASYNCHRONOUS DMA COMMAND COMPLETION NOTIFICATION WHEREIN THE DMA COMMAND COMPRISING A TAG BELONGS TO A PLURALITY OF TAG GROUPS	PEYTON,TAMMARA	YAMAZAKI, TAKESHI
11716845	Not Issued	20070168538	83	03/12/2007	Computer architecture and software cells for broadband networks	NGUYEN,THANH	YAMAZAKI, TAKESHI
11806429	7354205	20070280603	150	05/31/2007	ELECTRO-OPTICAL COMPOSITE CONNECTOR	MOONEY,MICHAEL	YAMAZAKI, TAKESHI
11834074	Not Issued	20080098198	30	08/06/2007	INFORMATION PROCESSING DEVICE, DATA TRANSFER METHOD, AND INFORMATION STORAGE MEDIUM	BRAGDON,REGINALD	YAMAZAKI, TAKESHI
11914170	Not Issued	0	17	01/07/2009	BRIDGE, PROCESSOR UNIT, INFORMATION PROCESSING APPARATUS, AND ACCESS CONTROL METHOD	.	YAMAZAKI, TAKESHI
11918033	Not Issued	20090079280	25	10/05/2007	Motor Apparatus		YAMAZAKI, TAKESHI
11919942	Not Issued	20090066183	30	11/05/2007	Motor, rotary electric machine and its stator, and method for manufacturing the stator	NGUYEN,TRAN	YAMAZAKI, TAKESHI
11921210	Not Issued	20090127943	25	11/27/2007	Electric Motor, Production Method Of Stator, And Production Method Of Electric Motor	.	YAMAZAKI, TAKESHI
12129128	Not Issued	0	30	05/29/2008	PRINTER	NGUYEN,JUDY	YAMAZAKI, TAKESHI
12130268	Not Issued	20080235647	30	05/30/2008	MODULAR DESIGN METHOD AND APPARATUS	DINH,PAUL	YAMAZAKI, TAKESHI
12175608	Not Issued	20090021766	25	07/18/2008	PRINT SYSTEM	.	YAMAZAKI, TAKESHI
12223465	Not Issued	20090128073	20	09/11/2008	Brushless Motor Control Method and Brushless Motor	.	YAMAZAKI, TAKESHI
12230374	Not Issued	20090060424	30	08/25/2008	Optoelectronic combination connector	LE,UYEN CHAU	YAMAZAKI, TAKESHI
12281268	Not Issued	20090009579	25	08/29/2008	Thermal Printer	.	YAMAZAKI, TAKESHI
12282391	Not Issued		19	01/01/0001	BRIDGE, INFORMATION PROCESSING SYSTEM, AND ACCESS CONTROL METHOD	.	YAMAZAKI, TAKESHI
12282393	Not Issued		19	02/11/2009	BRIDGE, INFORMATION PROCESSING DEVICE , AND ACCESS CONTROL METHOD	.	YAMAZAKI, TAKESHI
12316530	Not Issued	0	20	12/11/2008	Speckle reduction method	.	YAMAZAKI, TAKESHI
12331733	Not Issued	20090094388	20	12/10/2008	DMA Completion Mechanism	.	YAMAZAKI, TAKESHI
12379668	Not Issued	0	17	02/26/2009	Brushless motor	.	YAMAZAKI, TAKESHI

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12411992	Not Issued	0	25	03/26/2009	IMAGE ENCODING APPARATUS AND CONTROL METHOD THEREOF	,	YAMAZAKI, TAKESHI
12412002	Not Issued	0	25	03/26/2009	IMAGE ENCODING APPARATUS AND CONTROL METHOD THEREOF	,	YAMAZAKI, TAKESHI
60564647	Not Issued		159	04/22/2004	Method and apparatus for providing an interconnection network function	,	YAMAZAKI, TAKESHI
60564673	Not Issued		159	04/22/2004	Multi-scalar extension for SIMD instruction set processors	,	YAMAZAKI, TAKESHI
60564682	Not Issued		159	04/22/2004	Methods and apparatus for multi-processor pipeline parallelism	,	YAMAZAKI, TAKESHI
60564843	Not Issued		159	04/23/2004	Methods and apparatus for address map optimization on a multi-scalar extension	,	YAMAZAKI, TAKESHI
60564844	Not Issued		159	04/23/2004	Methods and apparatus for synthesizable pipeline control	,	YAMAZAKI, TAKESHI
60619069	Not Issued		159	10/15/2004	Methods and apparatus for supporting multiple configurations in a multi-processor system	,	YAMAZAKI, TAKESHI
60681082	Not Issued		159	05/13/2005	Methods and apparatus for resource management in a logically partitioned processing environment	,	YAMAZAKI, TAKESHI
61007569	Not Issued		159	12/13/2007	Projection system with a unit for reducing a speckle noise	,	YAMAZAKI, TAKESHI

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